

UXN6M9PE Eval Board

The divide ratio N is given by the following formula:

$$N = \sum_{k=0}^8 P_k \cdot 2^k = P_0 + P_1 \cdot 2 + P_2 \cdot 2^2 + P_3 \cdot 2^3 + \dots + P_8 \cdot 2^8$$

where $P_k = 0$ (Low) or 1 (High); valid for $N = 8, 9, \dots, 511$

Note 1:
Terminate un-used output in single-ended applications.

Note 2:
High = 0 V; closed
Low = -3.3 V; open

Differential Outputs:
100 Ohms,
SMA connectors

Differential Inputs:
50 Ohms

Centellax
UXN6M9P
6 x 6 mm² QFN

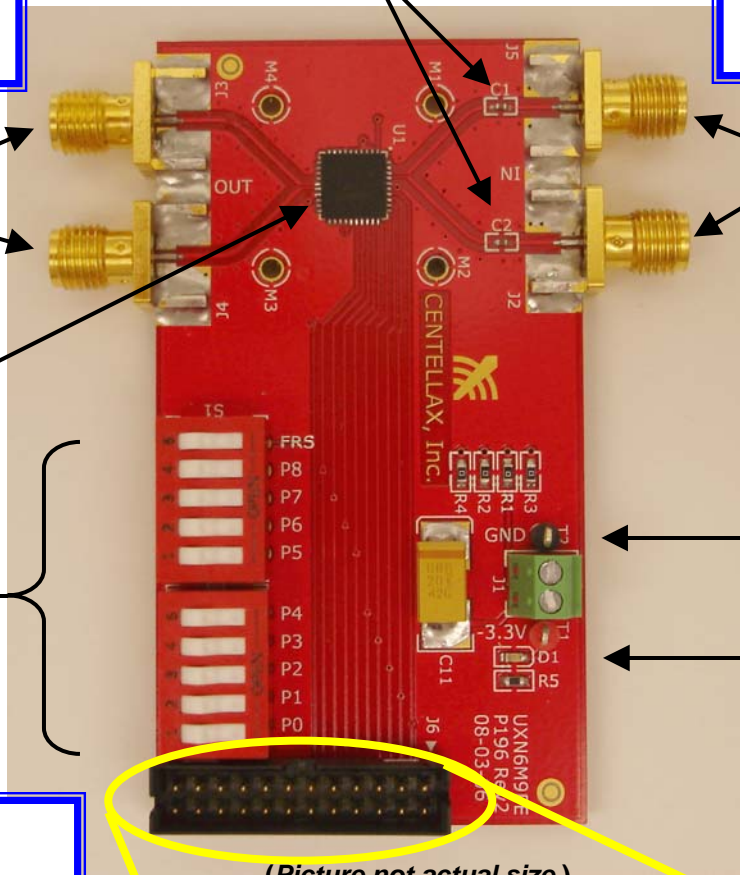
On-board switches to control the divide ratio N from 8 to 511.

Ground

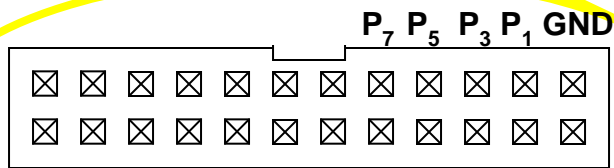
VEE = -3.3 V
I_{ee} ~ 150 mA

Note 3:
When using the parallel programming interface control, all on-board switches must be kept in the 'open' position. 'Open' corresponds to logic 'Low' or 0. Refer to the datasheet for logic level specs.

Note 4:
Set FRS=High for Fin > 6 GHz



(Picture not actual size.)



FRS P₈ P₆ P₄ P₂ P₀
(All other pins are not used.)

Dimensions

With connectors: 2.306" x 2.865" (58.57 mm x 72.79 mm)

Without connectors: 1.546" x 2.865" (39.28 mm x 72.79 mm)