

# UXN14M9PE Eval Board

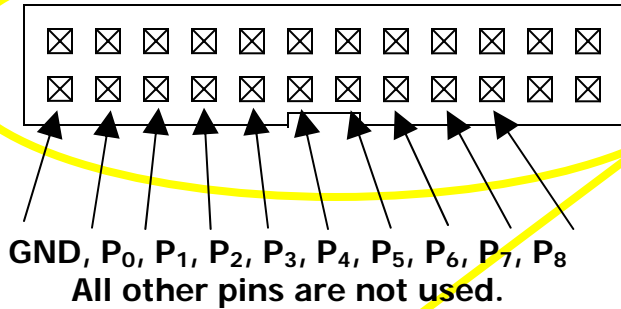


The divide ratio  $N$  is given by the following formula:

$$N = \sum_{k=0}^8 P_k \cdot 2^k = P_0 + P_1 \cdot 2 + P_2 \cdot 2^2 + P_3 \cdot 2^3 + \dots + P_8 \cdot 2^8$$

where  $P_k = 0$  (LO) or 1 (HI); valid for  $N = 8, 9, \dots, 511$

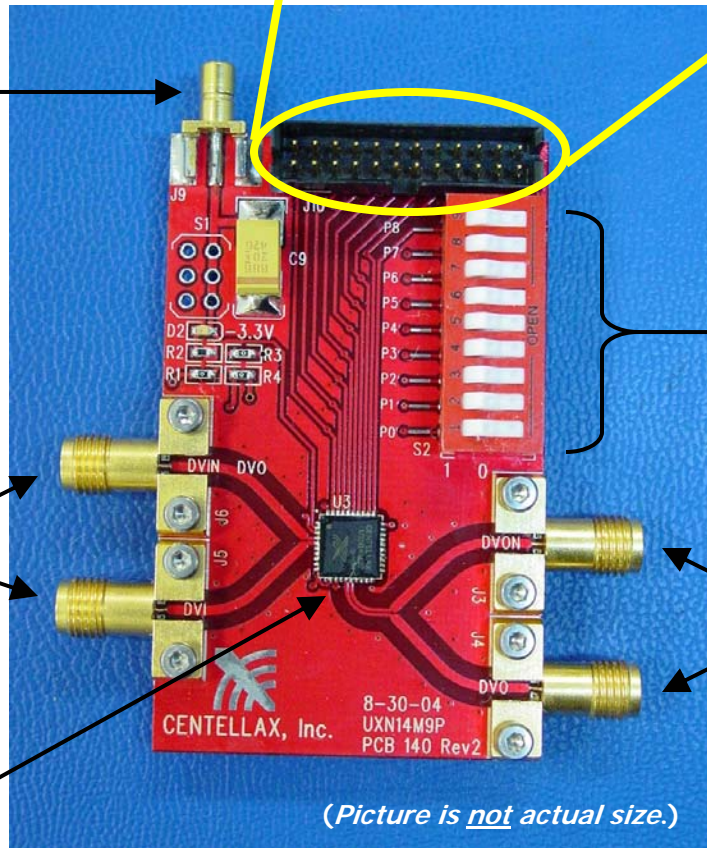
**Note:**  
When using the parallel programming interface control, the on-board switches must be kept in the 'open' position. 'Open' corresponds to logic 'LO' or 0. Refer to the datasheet for logic level specs.



VEE = -3.3 V  
I<sub>ee</sub> ~ 370 mA  
SMB connector

Differential Inputs:  
50 Ohms,  
SMA connectors

Centellax  
UXN14M9P  
6 x 6 mm<sup>2</sup> QFN



On-board switches to control the divide ratio  $N$  from 8 to 511.

Differential Outputs:  
R.L. ~ -10 dB  
SMA connectors

(Picture is not actual size.)

**Dimensions**

With connectors: 2.707" x 2.261" (68.76 mm x 57.43 mm)

Without connectors: 2.432" x 1.508" (61.77 mm x 38.30 mm)

**Note:**  
Terminate unused output in single-ended applications.