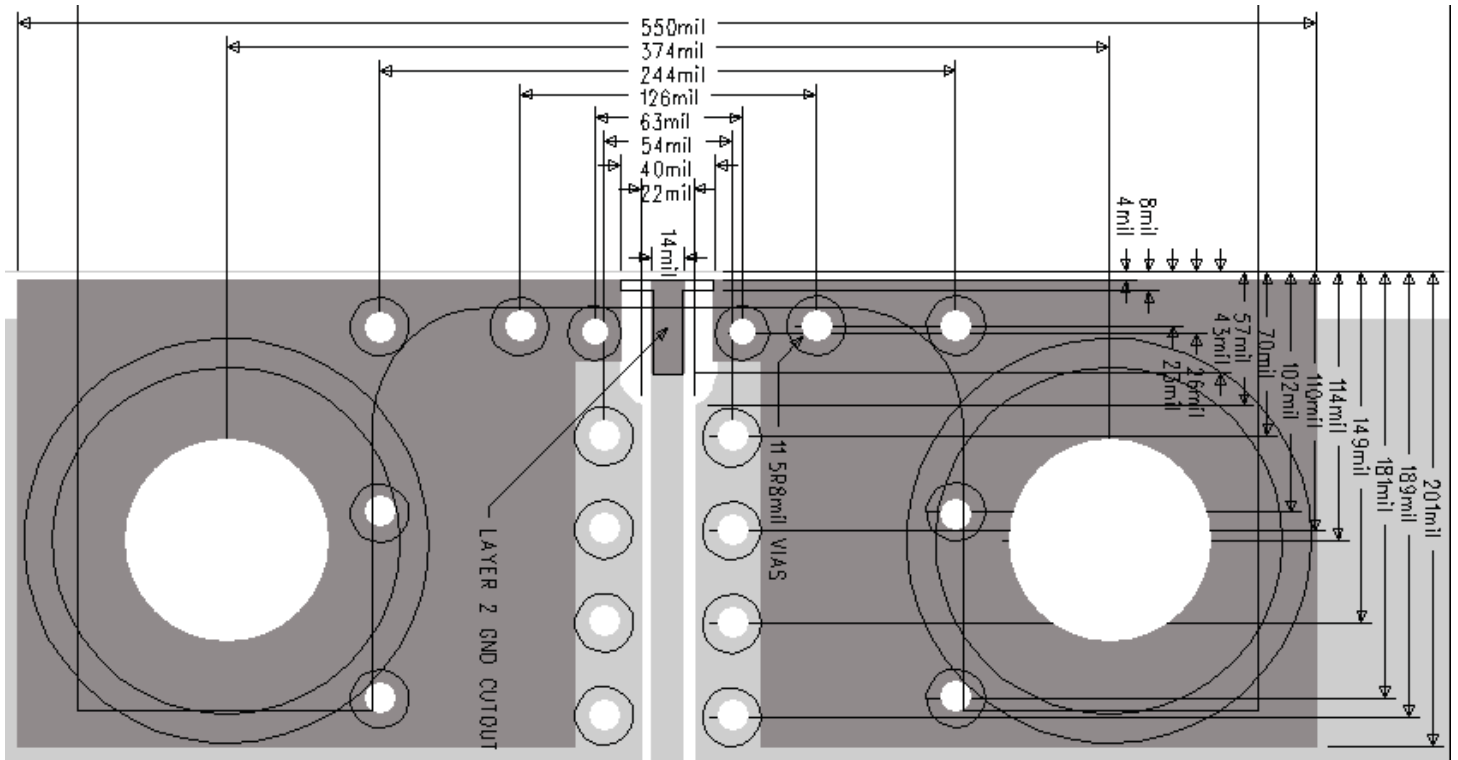
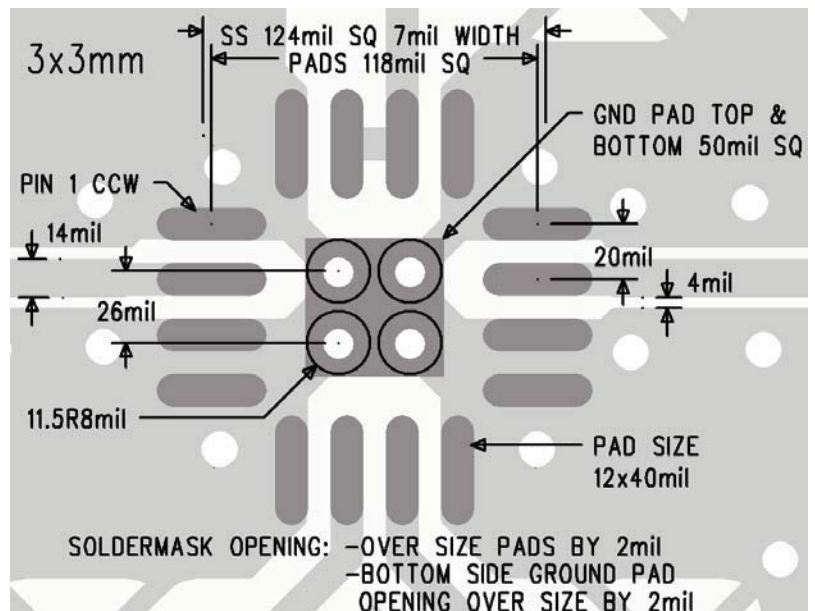
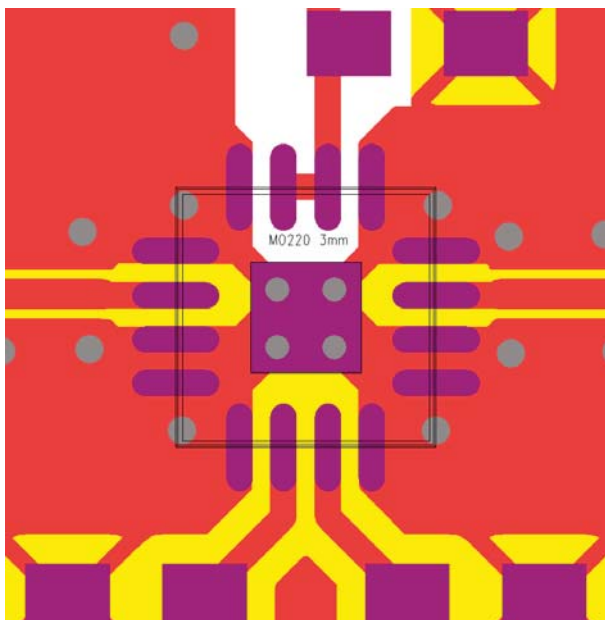


UA5M15MPE Evaluation Board

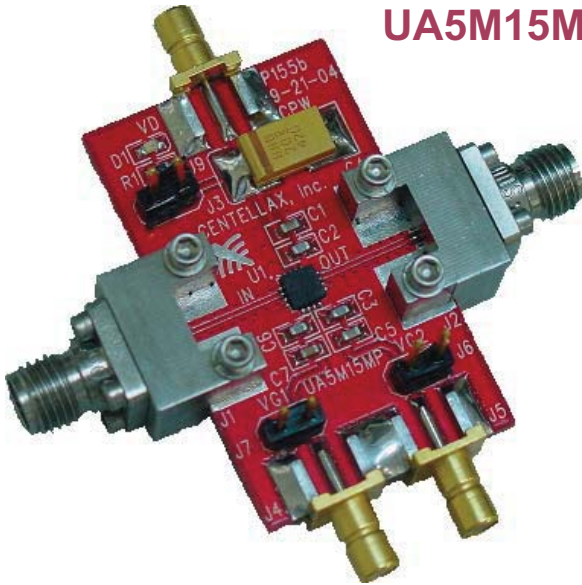
Evaluation Board Layout - Connector Attach Details



Evaluation Board Layout - QFN Land Details

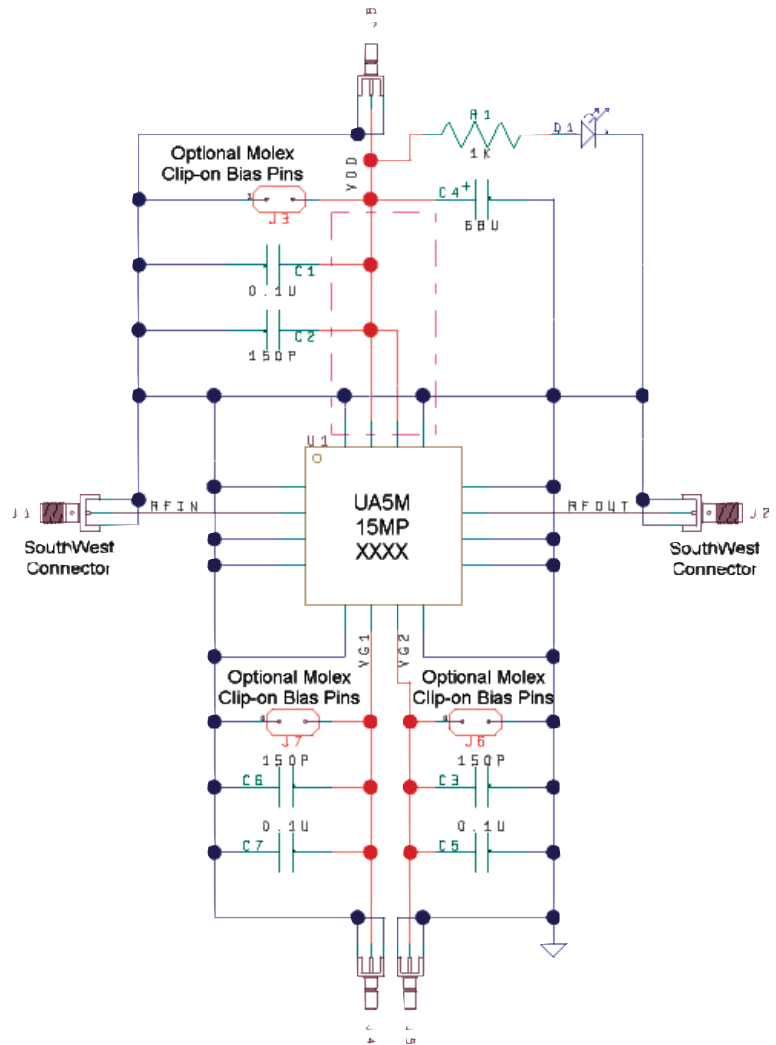


UA5M15MPE Evaluation Board



Evaluation Board Design Notes

1. UA5M15MP evaluation board is available upon request; part number is UA5M15MPE
2. PCB material is three-layer sandwich:
 - a. 8mil Rogers 4003; M1 above, M2 below
 - b. 39mil FR-4; M2 above, M3 below
 - c. 8mil Rogers 4003; M3 above, M4 below
3. SouthWest 2.92mm end-launch connectors are optimal for good performance
4. Follow Connector Attach Details for M2 ground plane cutaway under connector pin launch
5. 50Ω M1 GCPW line: W=14mil, S=4mil
6. M1 ground for CPW line is connected to M2 ground with a row of vias alongside the transmission line; see QFN Land Details
7. All QFN GND pads are connected to M1 ground and M2 ground (with vias)
8. PCB has solder mask to define pad locations
9. Solder paste and flux are screen-printed onto PCB for reflow attachment of QFN
10. PCB design recommendations and support is available from support@centellax.com



Evaluation Board Application Notes

1. Connect RFIN and RFOUT to 50Ω measurement system (connectors are SouthWest 2.92mm K-connectors, compatible with SMA)
2. Apply +3.3V or +5.0V drain bias voltage to VD1 and VD2 through the single SMB connector at the top of the board, or by connecting to the J3 Molex clip-on bias pins (see schematic)
3. Optionally connect 1st and/or 2nd stage gate bias voltages to VG1 and/or VG2 through the double SMB connectors at the bottom of the board, or by connecting to the Molex clip-on bias pins
4. Optionally adjust VG1 and/or VG2 for optimal application-specific performance